

Ph. D Admission (Jan 2018 Session)

List of Candidates shortlisted for Written Test /Interview to be held on 04.12.2017

Computer Science and Engineering

S.No	Application No	Name of Candidate	Category
1.	18010022	JOTHI PRASANNA .S	Vivesvaraya Scheme
2.	18010037	SUBIN SAHAYAM M	Regular HTRA
3.	18010041	ABIRAMI.T	Regular HTRA
4.	18010056	J DINESH KUMAR	Regular HTRA
5.	18010058	MANESH T	Regular HTRA
6.	18010066	MONIKA A	Regular HTRA
7.	18010074	SUMIT HAZRA	Regular HTRA
8.	18010078	VADAGA SUDHARSANA RAO	External
9.	18010085	EZHILMATHI S	Regular HTRA
10.	18010090	GERARD DEEPAK	Regular HTRA
11.	18010093	RAJESH R	Regular HTRA
12.	18010094	GORLI RAVI	Regular HTRA
13.	18010113	VENKATANAGASUDHEER T	Regular HTRA
14.	18010147	AMARESH M	Regular HTRA
15.	18010156	JOTHI PRASANNA . S	Regular HTRA
16.	18010173	SUMY MANUEL	Regular HTRA
17.	18010179	MUKKU NISANTH KARTHEEK	Regular HTRA
18.	18010183	POTHURI SOWMYA SUDHA	Regular HTRA
19.	18010187	JUNAID ALI RESHI	Regular HTRA
20.	18010202	SIVAGAMA SUNDARI R	Regular HTRA
21.	18010203	SAMEERA SHAIK	Regular HTRA
22.	18010215	PALLAVI	Regular HTRA
23.	18010218	PRAKASH K V S K	Regular HTRA
24.	18010245	BANDI PRASHANTH BABU	Regular HTRA
25.	18010249	WAGHODEKAR PRANATI PRABHAKAR	Regular HTRA
26.	18010252	P MARTINA MARIA PUSHPAM	Regular HTRA

27.	18010267	VANATHI M	Regular HTRA
28.	18010269	A.BEENA GODBIN	Regular HTRA
29.	18010278	JOE DHANITH P R	Regular NHTRA
30.	18010287	MAHENDRA KUMAR R	Regular HTRA
31.	18010289	SHANTHI D	Regular HTRA
32.	18010306	SONIKA C	Regular HTRA
33.	18010327	ANGEL G	Regular HTRA
34.	18010340	DEBENDRA MUDULI	Vivesvaraya Scheme
35.	18010364	BERYL G	Regular HTRA
36.	18010366	M MALLIKA	Regular HTRA
37.	18010367	PRABHAKARAN	Regular NHTRA
38.	18010368	SAISANTHIYA D	Vivesvaraya Scheme
39.	18010374	CHITRA M	Regular HTRA
40.	18010376	MALATHI V	Regular HTRA
41.	18010393	MANIGANDAN M	Vivesvaraya Scheme
42.	18010395	V S SINDUJA	Regular NHTRA
43.	18010416	SANTOSH KUMAR UPPADA	Regular HTRA
44.	18010426	ARUNIMA HOTA	Regular HTRA

Detailed Syllabus (Common for ECE and CSE)

Data Structures: ADT, Lists, Heaps, Binary trees, Graphs.

Algorithms: Time-complexity Analysis, solving recurrence relations, searching, sorting, algorithm design paradigms, basics of NP-completeness.

Computer Organization: Fixed/Floating point addition/subtraction/multiplication/division, addressing modes, cache memory (placement/replacement algorithms), pipelining, and hazards.

C-Programming: Iterative and recursive programs, structures, and pointers

Discrete Mathematics: (For CSE only) Sets, Relations, Functions, Counting, and First Order Logic, Proof Techniques, Automata theory.

Digital Signal Processing (For ECE only) Signals and Systems, transformations, filters, processor architectures, 2-D signal processing.



SAMPLE QUESTION PAPER

Objective: The focus of our admission test is to test your aptitude for research and problem solving skills.

Data Structures and Algorithm(Common for ECE and CSE)

1. What is the solution to the recurrence $T(n) = T(n - 1) + 2$, $T(1) = 2$
2. Solve: $T(n) = 4T(n/2) + n \log n$, $T(1) = 1$.
3. Say True or False.
 - $3n^2 + 4n + 1 = O(n^3)$
 - $n + 1 = \Omega(\log n)$
 - $n = \Omega(n^2)$
 - $n^2 + 1 = O(n)$
4. The worst-case time to search an element in a binary heap of height h with n elements is $O(n)$.
5. Given a binary tree with l leaves, find the number of internal nodes (including root) as a function of l .
6. Let A be a sorted linked list and A' be an unsorted linked list. Inserting an element x into A (the resultant A must still be a sorted list) is efficient than inserting x into A' . Say True or false.

Computer Organization (Common for ECE and CSE)

1. True or False: Addition of a non-negative number with a negative number will never generate a carry.
2. The following three lines of *partial* code is expected to swap two variables. Complete the code. (i) $a = a \text{ xor } b$ (ii) $b = b \text{ xor } a$ (iii)...
3. Perform Booth's Multiplication: 10100101×111
4. Identify the errors in the following code (it assumes 8085 instruction set)
 - MOV A,B
 - LOAD C,2000
 - MOV D,3000
 - MOV 2500,A
5. For the following specification with set associative mapping strategy, identify the number of bits in fields **Tag**, **set**, and **word**.
 - Cache size: 64 KB RAM size: 512 MB Block size: 4 KB Word size: 4 Bytes
 - 4-way Set-associative mapping

Programming/Algorithms(Common for ECE and CSE)

1. In the following Pseudo code, how many times the statements *for* and *print* are executed.
void fnprint()

```

{ for int i=1 to 5
  print i;
}

```

- Write a recursive program to print all permutations of a finite set.
- Is it possible to write a C-program to print the set of natural numbers. Is it possible to write a C-program to print the set of Real numbers. Justify.
- At the termination of the following program, what is the value of *count*.

```

int n; /*-- Input n--*/
count-function()
int i=1, count=0;
while (i ≤ n)
{
i = i * 2;
count ++;
}

```

- Given a finite array A , the function $\text{SWAP}(A[i], A[j])$ swaps the contents of $A[i]$ with $A[j]$ (Assume that the array is passed by reference). How do you use SWAP to reverse the contents of A .

Discrete Mathematics (For CSE only)

- Let $A = \{1, 2, 3\}$ and $R = \{(1, 2), (1, 3)\}$ be a binary relation defined on A . Is R transitive?
- Let $A = \{1, 2\}$ and $R = \{(1, 1), (2, 2)\}$ be a binary relation defined on A . Professor X says R is an equivalence relation. Is Professor X right?
- Count the number of permutations of the letters in 'admission'.
- Two logical statements S_1 and S_2 are defined as follows: S_1 : If $3+2=5$, then it rains on Sunday. S_2 : $3+2=5$. What logical inferences can you make from S_1 and S_2 .
- Count the number of one-one functions and onto functions (Assume the size of domain is m and the codomain is n).

Digital Signal Processing (For ECE only)

- Given $f \otimes g$ and also g , how do you find f ? where \otimes means convolution.
- What is the difference between convolution and correlation?
- Is mean filter linear? Justify your answer.

Instruction for Shortlisted candidates

- You are requested to report for Written Test cum Interview at 09.30AM on 04.12.2017 (Monday) in Indian Institute of Information Technology Design and Manufacturing Kancheepuram located at Melakottaiyur, Vandalur-Kelambakkam Road, Chennai-600127.
- **You are requested to produce the following certificates in Original along with one set of self attested copy of certificates at the time of Interview for verification:**
 - Diploma, Degree Certificates or Provisional Certificates from the Competent Authority of University or Institute concerned. In case of candidates whose results are awaited, producing the marksheets / grade cards will be sufficient. However if selected, they have to produce the certificate mentioned for verification at the time of Admission.
 - All Mark Statements / Grade Cards of qualifying Examinations.
 - GATE Score Card / UGC / CSIR and Other fellowship award letter (as the case may be)
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Ph.D. Admission (Jan-2018 Session)

List of Shortlisted Candidates for Written Test / Interview

Electrical / Electronic Engineering

Sl. No	App.No	Name	Stream
1.	18010005	P ANITHA	Regular HTRA
2.	18010006	P ANITHA	Regular NHTRA
3.	18010040	ANISH JOY	Regular HTRA
4.	18010088	MOUPURI SATISH KUMAR REDDY	Regular HTRA
5.	18010115	NANDAKUMAR BALIREDDIGARI	Regular HTRA
6.	18010128	SATHISH KUMAR T	Regular HTRA
7.	18010152	KONETI SANDEEP	Regular HTRA
8.	18010171	SURESH A K	Regular HTRA
9.	18010182	GANDRETI RAVI KISHORE	Regular HTRA
10.	18010193	JOHNSON ANTONY A	Regular HTRA
11.	18010196	PRASANNA	Regular HTRA
12.	18010199	PRAKASH CHANDRA JOSHUA C	External
13.	18010229	SRIDHAR MAKKAPATI	Regular HTRA
14.	18010232	GAMASU RAMESH	External
15.	18010239	MANTHINA SATYANARAYANA RAJU	Regular HTRA
16.	18010247	SIRIGIREDDY PRAVALLIKA	Regular HTRA
17.	18010273	CHANDRA PRABHA G	Regular HTRA
18.	18010277	P.KANAKARAJ	Regular HTRA
19.	18010283	AKHILA K	Regular HTRA
20.	18010288	MUTHUKUMAR.T	Regular HTRA
21.	18010291	ANNABATTULA JAWAHAR	Regular HTRA
22.	18010292	ANNABATTULA JAWAHAR	Regular NHTRA
23.	18010293	MURUGESAN	Regular HTRA
24.	18010294	ANNABATTULA JAWAHAR	External
25.	18010301	KUMAR SAURABH	Regular HTRA
26.	18010308	UDHAYAKUMAR E	Regular HTRA
27.	18010318	PAZHANIVEL A	Regular HTRA
28.	18010320	ANISH KUMAR S A	Regular HTRA
29.	18010344	MANIGANDAN M	Regular HTRA
30.	18010378	SUDHAKAR C	Regular HTRA
31.	18010384	SRIDHARAN K	Regular HTRA

32.	18010388	PUSHPA P	Regular NHTRA
33.	18010391	MEERA MOHIDDIN SHAIK	External
34.	18010397	KL NAGA SADEEP KUMAR	Regular HTRA
35.	18010407	K M ABHIJITH	Regular HTRA
36.	18010417	NAVEENKUMAR TADIKONDA	Regular HTRA
37.	18010026	VIGNESWARAN T	Regular HTRA
38.	18010028	ABEL GEORGE	Regular HTRA
39.	18010052	VINOTHINI S	Regular HTRA
40.	18010007	SELVA PRIYA	Regular HTRA
41.	18010302	SARANYA B	Regular HTRA
42.	18010017	CHANDRAPRABHA	Regular HTRA
43.	18010025	DUDDUUKURI SIVA SAI SRIKAR	Regular NHTRA
44.	18010027	GOWRI MANOHARI R	Regular HTRA
45.	18010044	SUBRAMANIAN	External
46.	18010048	PAKALA SAI KRISHNA	Regular HTRA
47.	18010054	J DINESH KUMAR	Regular HTRA
48.	18010055	K A NAVEEN KUMAR	Regular HTRA
49.	18010065	SANDEEP Y	Regular HTRA
50.	18010067	VINOTH KUMAR S	Regular HTRA
51.	18010071	SIVASHANKAR	Sponsored Project
52.	18010076	RAYEES AKMAL	Regular HTRA
53.	18010107	SARANYA DEVI	Regular HTRA
54.	18010117	D SENTHIL KUMARAN	External
55.	18010118	MEERA DEVI R V	Regular HTRA
56.	18010122	MAHESWARI R	Regular HTRA
57.	18010129	SAI RAJANARENDRA	Regular HTRA
58.	18010133	ANITHA V	Regular HTRA
59.	18010151	YOGANATHAN M	Regular HTRA
60.	18010164	LAKSHMI G	Regular HTRA
61.	18010165	PRABHU P	External
62.	18010166	KAVITHA C	Regular HTRA
63.	18010198	BENITTA SHERLIN.J	Regular HTRA
64.	18010223	SUBODH RAJ M. S.	Regular HTRA
65.	18010226	A.ANILET BALA	Regular HTRA
66.	18010259	PRATAP KUMAR DAKUA	Regular HTRA
67.	18010262	INDLA VIDYASAGAR	Regular HTRA
68.	18010276	ARCHANA T	Regular HTRA
69.	18010282	SHYNI G	Regular HTRA

70.	18010305	DEVI SOWJANYA P	External
71.	18010307	K KANNAIAH	Regular HTRA
72.	18010311	SIVASAKTHIVEL	Regular HTRA
73.	18010313	M MANIGANDAN	Regular NHTRA
74.	18010315	VALI BASHA PINJARI	Regular HTRA
75.	18010321	MOHAMMAD SAADH AW	Regular NHTRA
76.	18010332	S. MANIKANDASWAMY	Regular HTRA
77.	18010333	KILARI PARDHASARDI	Regular HTRA
78.	18010335	T R JAYASHREE	Regular HTRA
79.	18010336	REJI G	Regular HTRA
80.	18010338	SINDHURA	Regular HTRA
81.	18010346	A.SUVERNAMMA	Regular HTRA
82.	18010351	PARTHASARATHY P	Regular HTRA
83.	18010359	SUKANTA KUMAR SWAIN	Regular HTRA
84.	18010362	DEEPIKA C	Regular HTRA
85.	18010363	KALAIMAGAL G	Regular HTRA
86.	18010375	MAHESWARI R	External
87.	18010409	ANITHA M R	Regular HTRA
88.	18010410	PRIYANK MISHRA	Regular HTRA
89.	18010419	ELANGO VAN K	Regular HTRA
90.	18010421	POOJA PRAKASH	Regular HTRA

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Ph.D. Syllabus

Important Note:

1. The candidate can choose the research area based on educational background.
2. There will be two evaluation parts: (i) written test (i) interview
3. The total time duration for written test will be **one hour**.
4. The total marks for written test will be **50**.
5. The written test generally begins at **9.30 AM** in the **Academic Complex Ground floor** of the IIITDM Kancheepuram campus.

Four Research Groups and Syllabus

Research Groups	Syllabus
EE1 : Communications/Networking/Signal Processing	<p>Communication/Networking:</p> <ul style="list-style-type: none">• Basics Concepts of Probability Theory• Basics Concepts of Linear Algebra• Analog and Digital Communication <p>Signal Processing:</p> <ul style="list-style-type: none">• Review of Signals and systems• Discrete time signals and systems• Z-transform• Discrete Fourier transform• Computation of the DFT and the Fast Fourier Transform• Sampling of Continuous-time Signals• Transform Analysis of Linear Time Invariant Systems

<p>EE2: Microelectronics / VLSI / MEMS</p>	<p>Microelectronic Circuits:</p> <p>Diodes and applications, MOSFET and BJT amplifier circuits, Ideal Op amp, arithmetic circuits and other applications using Op amp. Boolean Algebra, Combinational circuit design, Sequential circuit design, Arithmetic circuits design, Memory design, CMOS Inverter, DC and Transfer characteristics, propagation delay, power dissipation.</p>
<p>EE3: Power Electronics/Controls *</p> <p>(*Candidate can choose either Paper-1 or Paper-2)</p>	<p style="text-align: center;"><u>Paper-1</u></p> <p>Operation of Buck, Boost and Buck-boost converters, Power semiconductor devices and characteristics, Operation of inverters – 180 degree mode and SPWM operation – single phase and three phase, AC to DC bridge converters – single and three phase.</p> <p>Fourier series and Laplace transforms, DC, Single phase and three phase AC Circuit Analysis and Network Theorems.</p> <p>Theory and operation of DC machines, Induction machines and PM synchronous machines (BLDC and PMSM). Electric Drives – Power electronic control of DC & Induction machines.</p> <p>Transfer function, transient and steady state response, Routh-Hurwitz, Bode plots, Root loci, Stability analysis, P, PI and PID controllers, State space model, State transition matrix, eigen values. Time and frequency domain analysis.</p> <p style="text-align: center;"><u>Paper -2</u></p> <p>Transfer function, transient and steady state response, Routh-Hurwitz, Bode plots, Root loci, Stability analysis, P, PI and PID controllers, State space model, State transition matrix, eigen values. Time and frequency domain analysis, Controllability, Observability, Stability and pole-placement.</p> <p>Fourier series and Laplace transforms, D C, Single phase and three phase AC Circuit Analysis and Network Theorems,</p> <p>Electrical and Electronic Measurements: Static and dynamic characteristics of Measurement Systems. Bridges and potentiometers, measurement of R, L and C. Measurements of voltage, current, power, power factor and energy. Time, phase and frequency measurements. Resistive, Capacitive, Inductive and piezoelectric transducers and their signal conditioning. Measurement of displacement, velocity and acceleration (translational and rotational), force, torque, vibration and shock. Measurement of pressure,</p>

	flow, temperature and liquid level. Measurement of pH, conductivity, viscosity and humidity.
EE4: RF	Vector algebra, Electromagnetic theory-transmission lines-impedance matching-smith chart operations-scattering parameters-network parameters-antenna basics.

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Ph.D. Admission (Jan-2018 Session)

List of Shortlisted Candidates for Written Test / Interview

Physics

to be held on 06.12.2017 at 9.30 AM)

Sl.No	App.No	Name	Stream
1.	18010170	ATHIRA K S	Regular HTRA
2.	18010325	ASHOK KUMAR R	Regular HTRA

Syllabus for the written test in Physics

- Classical Mechanics
- Quantum Mechanics
- Electrodynamics
- Optics
- Solid State Physics

Duration of test: 2 Hrs

Instruction for Shortlisted candidates

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- **You are requested to produce the following certificates in Original along with one set of self attested copy of certificates at the time of Interview for verification:**
 - Diploma, Degree Certificates or Provisional Certificates from the Competent Authority of University or Institute concerned. In case of candidates whose results are awaited, producing the marksheets / grade cards will be sufficient. However if selected, they have to produce the certificate mentioned for verification at the time of Admission.
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Ph.D. Admission (Jan-2018 Session)

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Mechanical Engineering

Sl. No	App.No	Name	Stream
1.	18010004	SRIDHAR E	Regular HTRA
2.	18010010	KAMEPALLI ANJANEYULU	Regular HTRA
3.	18010013	A PAVAN KUMAR	Regular HTRA
4.	18010034	GABRIEL A	Regular HTRA
5.	18010042	DHANAPAL S	Regular HTRA
6.	18010046	MADHANAGOPAL M	Regular HTRA
7.	18010047	SUJITH M	Regular HTRA
8.	18010051	RAJENDRA KUMAR R T P	Regular HTRA
9.	18010064	ABDULLAH SULTAN F	Regular HTRA
10.	18010073	ATHIRA K S	Regular HTRA
11.	18010075	SUBRAMONIAM A	Regular HTRA
12.	18010080	GADHE ARAVIND REDDY	Regular HTRA
13.	18010091	MIDUNKUMAR V	Regular HTRA
14.	18010092	MANIMARAN M	Regular HTRA
15.	18010121	PARANTHAMAN V	Regular HTRA
16.	18010141	PRADEEPKUMAR D	Regular HTRA
17.	18010143	RAJALINGAM A	Regular HTRA
18.	18010153	RENURAMAN J	Regular HTRA
19.	18010154	KATHIRVEL R	Regular HTRA
20.	18010162	VEERA PRABAKARAN	Regular HTRA
21.	18010168	K C CHARAN	Regular HTRA

Sl. No	App.No	Name	Stream
22.	18010176	SENTHILKUMARAN. S	Regular HTRA
23.	18010181	GANGADHARI RAJAN KUMAR	Regular HTRA
24.	18010186	V.ELUMALAI	Regular HTRA
25.	18010201	PUTTA RAMUDU	Regular HTRA
26.	18010216	PARI G	Regular HTRA
27.	18010219	P DURGA PRASAD	Regular HTRA
28.	18010221	SHIVANANDA B K	Regular HTRA
29.	18010230	N VIJAY KRISHNA	Regular HTRA
30.	18010231	KATHIR KAMAN M D	Regular HTRA
31.	18010234	SHAIK SADDAM HUSSAIN	Regular HTRA
32.	18010236	G GANGARAJU	Regular HTRA
33.	18010238	S. ABDUL MUTALIB	Regular HTRA
34.	18010241	G DEVARAJ	Regular HTRA
35.	18010246	SATHISHKUMAR S	Regular HTRA
36.	18010253	RAGHURAMAN T	Regular HTRA
37.	18010261	J JENSIN JOSHUA	Regular HTRA
38.	18010266	ASSARUDEEN H	Regular HTRA
39.	18010280	RAMARAJAN J	Regular HTRA
40.	18010286	KARTHEESAN S	Regular HTRA
41.	18010297	BALASUBRAMANI P	Regular HTRA
42.	18010323	DEVANAND S	Regular HTRA
43.	18010330	JOHNSON JOSEPH JEBAKUMAR B	Regular HTRA
44.	18010334	RAGHULNATH D	Regular HTRA
45.	18010350	G GOPI	External

Sl. No	App.No	Name	Stream
46.	18010357	SIVASHANKAR	Regular HTRA
47.	18010371	MAHENDRA BABU MEKALA	External
48.	18010389	PRIYARANJAN M	Regular HTRA
49.	18010392	SURESH BABU K	Regular HTRA
50.	18010396	MOHAMED FERAZ A	Regular HTRA
51.	18010411	THAJTHEEN Y	Regular HTRA
52.	18010057	ANAND N	Regular HTRA
53.	18010033	SATHEESHKUMAR V	Sponsored project of the University of Genova, Italy
54.	18010347	SRIDHAR E	Sponsored project of the University of Genova, Italy

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 - Printed Copy of Application generated through Online Registration.
- In case of discrepancy in the data provided online, you will not be permitted to attend Written Test / Interview and No TA / DA will be reimbursed.
- Outstation candidates who have qualified for Interview under Regular (Full Time) with HTRA category based on written test are eligible for reimbursement of sleeper class single to and fro rail fare from their place of communication address to Chennai by the shortest route on submission of all relevant information and documents. **Only one claim is admissible even in case of attending more than one Interview at this Institute in same trip and the admissible amount will be paid through RTGS by the Institute. In this regard, candidates are advised to provide your account detail along with claim.**
- Kindly confirm your participation for Written Test cum Interview by email to admissions@iiitdm.ac.in. The Written Test cum Interview cannot be postponed under any circumstances. The syllabus and other information are uploaded in our website: www.iiitdm.ac.in.
 - In case of final selection, candidates are required to send their acceptance together with the fees for the first semester on or before 26th December 2017. Academic session for Jan-May 2018 semester will commence on 01st January 2018 onwards.

Syllabus for Mechanical Engineering

Written test (Duration - 2 hours)

Part I – Basic Engineering Sciences - 50 marks - 1 hour (**Compulsory for all**)

Part II - 50 marks – 1 hour (Any one of the following Parts)

Part II A – Engineering Design

Part II B – Materials and Manufacturing Processes

Part II C – Thermal Engineering

Part II D – Sponsored Project of University of Genova, Italy

Part I – Basic Engineering Sciences (Common to all)

Engineering Mathematics: Fundamentals of Linear algebra, Calculus, Differential equations, Probability and Statistics, and Numerical methods.

Engineering Graphics: Plane curves, projection of lines, planes and solids, section and development of solids, and isometric projection.

Engineering Mechanics: Free body diagram, equilibrium of particles and rigid bodies, friction, centroid, virtual work, kinematics and dynamics of particles and of rigid bodies in plane motion.

Part II A – Engineering Design

Mechanics of materials: Theory of pure bending moment and torsion, deflection of beams, Euler theory of long columns, design of thin and thick pressure vessels, principal stress strain, combined bending and torsion, application of failure theories in design, design of mechanical components such as shafts, gears, cams and followers, design of mechanical joints and drives, application of fits and tolerances in design.

Kinematics and dynamics of machines and mechanisms: position-velocity-acceleration analysis, classification of robots and mechanisms, mapping and transformations in robotics, differential motion- Jacobian-trajectory analysis, design of robot mechanisms for various applications.

Part II B – Materials and Manufacturing Process

Engineering Materials: Structure and properties of engineering materials, phase diagrams, heat treatment, stress-strain diagrams for engineering materials.

Machining and Machine Tool Operations: Mechanics of machining; basic machine tools; single and multi-point cutting tools, tool geometry and materials, tool life and wear; economics of machining; principles of non-traditional machining processes; principles of work holding, design of jigs and fixtures.

Metrology and Inspection: Limits, fits and tolerances; linear and angular measurements; comparators; gauge design; interferometry; form and finish measurement; alignment and testing methods; tolerance analysis in manufacturing and assembly.

Part II C – Thermal Engineering

Heat-Transfer: Modes of heat transfer; one dimensional heat conduction, resistance concept and electrical analogy, heat transfer through fins; unsteady heat conduction, lumped parameter system, Heisler's charts; thermal boundary layer, dimensionless parameters in free and forced convective heat transfer, heat transfer correlations for flow over flat plates and through pipes, effect of turbulence; heat exchanger performance, LMTD and NTU methods; radiative heat transfer, Stefan Boltzmann law, Wien's displacement law, black and grey surfaces, view factors, radiation network analysis.

Thermodynamics: Thermodynamic systems and processes; properties of pure substances, behavior of ideal and real gases; Zeroth and first laws of thermodynamics, calculation of work and heat in various processes; second law of thermodynamics; thermodynamic property charts and tables, availability and irreversibility; thermodynamic relations.

Part II D – Sponsored Project of University of Genova, Italy

Robot classification and anatomy, forward and inverse kinematics, DH matrix transformation, Jacobian and differential motion, Trajectory planning, Static and dynamic analysis, Force and motion control, Application of robots and sensors in manufacturing, Mobile robots and swarm control. AI techniques and integration of IoT in robotics.