

IIITDM Kancheepuram - Recruitment of Non-Teaching Posts

(Advt.No.IIITDMK/R/3/2019 Dated: 19th June, 2019)

List of Shortlisted Candidates for screening test and Schedule of exams for the post of Junior Technical Superintendent (ECE)

Qualification and experience	BE / B.Tech in Electronics / Electronics and Communication Engineering with 5 year experience.
Desirable	Post qualification experience in Networking / System Maintenance /Computer Applications / Maintenance of Electronic Lab Equipment ,etc

Application Numbers of Shortlisted Candidates			
1903JTSE0001	1903JTSE0033	1903JTSE0056	1903JTSE0075
1903JTSE0003	1903JTSE0036	1903JTSE0058	1903JTSE0077
1903JTSE0004	1903JTSE0037	1903JTSE0059	1903JTSE0081
1903JTSE0006	1903JTSE0039	1903JTSE0061	1903JTSE0082
1903JTSE0008	1903JTSE0040	1903JTSE0062	1903JTSE0084
1903JTSE0009	1903JTSE0041	1903JTSE0066	1903JTSE0085
1903JTSE0012	1903JTSE0042	1903JTSE0067	1903JTSE0087
1903JTSE0014	1903JTSE0047	1903JTSE0069	1903JTSE0089
1903JTSE0015	1903JTSE0048	1903JTSE0070	1903JTSE0090
1903JTSE0019	1903JTSE0049	1903JTSE0071	1903JTSE0091
1903JTSE0027	1903JTSE0050	1903JTSE0072	1903JTSE0092
1903JTSE0031	1903JTSE0051	1903JTSE0073	1903JTSE0093
1903JTSE0032	1903JTSE0053	1903JTSE0074	

Schedule:

Level 1 exam :	17.12.19 (Tuesday) - 10:00 AM (Reporting Time: 08: 30 AM)	
Level 2 exam :	18.12.19 (Wednesday) - 10:00 AM	<i>Only for candidates qualified in Level 1 exam</i>
Level 3 exam :	18.12.19 (Wednesday) - 02:00 PM	

Note:

- All the shortlisted candidates are required to bring original mark sheets (SSLC onwards), degree certificates, experience certificates and valid community certificate (SC/ST/OBC/EWS) at the time of reporting. Employees serving in Govt. Organizations /autonomous bodies are to submit No Objection Certificate from the present employer. Only the Candidates satisfying the requirement will be permitted to attend the written Test.

Scheme of Examination

I. Levels of Exams:

Level 1:

All the shortlisted candidates shall write screening test carrying maximum of 100 Marks (Objective type).
Maximum duration of exam: 2 hours

Level 2:

It is the subject knowledge test designed to test the candidate's knowledge in the concerned subject of specialization. The questions will be objective/descriptive type carrying maximum of 100 marks.
Maximum duration of exam: 1 hour 30 minutes

Level 3:

It is the trade test to assess the practical knowledge of the candidate in the concerned subject carrying maximum of 100 marks.
Maximum duration of exam: 1 hour 30 minutes

** The standard of questions for the tests will generally be in conformity with education standard prescribed for the post.*

II. Weightage of exams:

Level 1:

Candidates securing the minimum qualifying marks shall be shortlisted for further evaluation process scheduled on the next day. In case of SC/ST candidates, the minimum qualifying marks is relaxable at the discretion of the competent authority. The marks secured in the screening test shall not be taken into account for preparation of final selection list.

Level 2 & Level 3:

Level 2 and Level 3 are of qualifying nature and merit list will be prepared based on the following allocation of weightage.

Level 2 : 60% and Level 3: 40%

*** In case of tie, suitable criteria decided by duly constituted committee will be followed.*

Note:

Success in the examination confers no right of appointment unless the candidate fulfills all requirements of the institute.

Syllabus of Examination

Level 1:

Verbal Reasoning & Aptitude, General English, General Knowledge, Current affairs

Level 2:

Analog and Digital Electronics: Diode and Wave shaping circuits, Bipolar Junction Transistor, MOSFET, Zener diode, Oscillators, Power Amplifier, BJT differential amplifier, non-ideal characteristics of differential amplifier, Phase Locked Loops, Operational Amplifiers, Boolean Algebra, Logic Gates and Networks, Combinational Circuit and sequential circuits, Multiplexers, Decoders, Encoders, Code Converters, arithmetic Comparators Circuits. Flip-Flops, Registers and Counters, Design of Counter using Sequential Circuit Approach.

Network Analysis: Network Elements and theorems, network analysis, Laplace Transform, Transient analysis in DC circuit using Laplace domain transformation method and State Variable Analysis, Fourier Series and System /Network Characterization, Frequency response, Two-port network analysis, basics of control system: open/closed loop systems and stability

Electronics Measurement and Instrumentation: Static and Dynamic Characteristics, Test instruments, multi meter, function generator, Digital storage Oscilloscope, Measurement of different quantiles, Transducers and sensors

Microprocessors and Microcontroller: Overview of 8085 microprocessors, Interfacing with peripherals :8155, 8255, 8254, 8279, 8259, etc. Interfacing with keyboards, 8086 microprocessors, Intel Microprocessors :Pentium Series, i-series., Embedded and Processor Technology, Microcontroller 8051, Peripherals, Programmable Logic Controllers)PLCs

Communication: Elements of Communication systems, Information sources and communication channels, Modulation, Multiplexing, FM Demodulators, AM and FM receiver. Super Heterodyne Radio receiver, Sampling Theorem, Nyquist rate & Aliasing, Sampling, PAM, PWM & PPM system Time Division Multiplexing, Delta and Adaptive delta modulation. Digital communication system, Spread Spectrum, DS-SS & FH-SS, Shannon's Theorem and channel capacity.

Signal Processing: Continuous-time and discrete-time signals and systems, Continuous-time Linear Time-invariant (LTI) system, Discrete-time LTI system, Fourier series representation of continuous-time periodic signals, the Continuous-time Fourier Transform, DTFT, DFT, Z-transform.

Level 3:

Practical test based on the above (Level-2) topics.