Annexure VI

INTRODUCTION OF NEW COURSE

Course Title	VLSI DATA CONVERSION CIRCUITS	Course No						
Specialization	Electronics Engineering	Structure (LTPC)	3	0	0		3	
Offered for	UG/PG/PhD	Status	Core		Electi	ive		
Faculty	Dr. Binsu J Kailath	Туре	New		Modi	fication		
Pre-requisite		To take effect from	May 2015					
Submission date	April 2015	Date of approval						
Objectives	This course introduces the basics of Analog to digital and digital to analog conversion circuits in VLSI. After familiarizing with the basics of data converters, the students are introduced to Switched capacitor circuits and Flash ADCs. Need and benefits of oversampling approach and delta sigma modulators are discussed in detail. As these circuits are essential building blocks for any real time signal processing systems, this course would help them to design such systems for a set of specifications.							
Contents of the course	Sampling, over sampling, time interleaved sampling, sample and hold circuits, distortion, thermal noise, charge injection(6)							
(Switched capacitor circuits, non idealities, fully differential SC circuits (5)							
	ADC terminology offset and gain error, non linearity, dynamic characterization, quantization noise (4)							
	Flash ADC, regenerative latch, preamp, offset correction, differential pre amp, pre amp design, timing issues(7)							
	DAC basics, architectures, binary weighted DAC, current steering DAC (4)							
	Over sampling approach, signal and noise transfer functions, Delta Sigma converters, stability in Delta sigma modulators (6)							
	Continuous time delta sigma modulators, anti aliasing property, effect of opamp non-idealities, loop filter(10)							
Text and	Text Books							
References	Academic Publishers, ISBN-0-79	, , , , , , , , , , , , , , , , , , , ,						
	References							
	 Delta-Sigma Data Converters: Theory, Design, and Simulation by Steven R. Norsworthy, Richard Schreier, Gabor C. Temes, Wiley-Blackwell, ISBN-13: 978-0780310452, 1996 Understanding Detla- Sigma Converters – by Richard Schereier, Gabor C. Temes IEEE Journal of Solid State Circuits for the state of the art circuits 							