

PREAMBLE

In furtherance of the vision of Aatmanirbhar Bharat and positioning India as the global hub for Electronics System Design and Manufacturing, the Govt. of India has recently approved the comprehensive program for the development of sustainable semiconductor ecosystem in the country. The Govt. aims to step towards leapfrogging in the ESDM space by way of inculcating the culture of System on Chip and System Level Design at Bachelors, Masters and Research level and act as a catalyst for growth of start-ups involved in fabless design. In order to make this a reality, huge talent pool of high skilled engineers in VLSI domain needs to be created and this workshop is just a humble beginning in that direction. ASIC Design lowers the cost and size of an electronic circuit or system by miniaturizing and combining separate components and their functions into a single unit.

OBJECTIVES

- To familiarize with the major/basic steps in the Testing and Verification of ASIC
- To model Faults and perform Simulation
- To familiarize with the concepts of Design for Testability, BIST and ATPG
- Basic System Verilog: Data, Operators, Structures, Hierarchy, Connectivity, Interfaces, functional, code, line and FSM coverage
- Advanced system Verilog: Classes, Polymorphism and Virtuality, Interfaces, QDA, ABV and SVA

The sessions in the workshop will be handled by faculty members from IIITDM Kancheepuram /IITs /NITs/IIITs as well as experts from Industries.

ABOUT IIITDM KANCHEEPURAM

Indian Institute of Information Technology Design and Manufacturing (IIITDM) Kancheepuram Chennai (www.iiitdm.ac.in) is an Institute of National Importance established in 2007 by the Ministry of Education, Government of India to pursue design and manufacturing oriented engineering education and research and to generate design specialists with strong fundamentals in their core engineering to demonstrate competitive advantage of Indian engineering products in global markets. In addition to the B Tech, M Tech and M Des programmes, the Institute offers PhD programmes in Computer Science and Engineering, Electronics and Communication Engineering, Mechanical

Engineering, Interdisciplinary Product Design, Mathematics, Physics and English. It's located on a 51-acre campus on the outskirts of Chennai, off the Vandalur-Kelambakkam road.

ABOUT THE DEPARTMENT

The Department of Electronics and Communication Engineering at IIITDM Kancheepuram offers B.Tech programme in Electronics and Communication Engineering, Dual Degree programme leading to B.Tech and M.Tech Degrees, M.Tech programmes in Communication Systems, Microelectronics and VLSI Systems and Ph.D. programmes in core and allied areas of Electronics and Communication Engineering in addition to Electrical Engineering. The Department consists of a vibrant team of faculty members working in diverse areas including VLSI, Power Systems, Signal Processing, Communication, RF and Photonics. The faculty members are involved in sponsored and consultancy research projects from reputed industries and government organizations.

ELIGIBILITY

Faculty members, Research Scholars, Students and Industrial participants can apply.

PAYMENT AND REGISTRATION

The participants are requested to register online by paying Rs. 679/- as detailed below. Following link may be used for making payment.

<https://www.onlinesbi.com/sbicollect>

Proceed as follows:

SBI collect - State (TN) –

Educational Institute (*IIITDM Kancheepuram*)

Choose (*IIITDM Educational Event*)

Short name of workshop/STTP (*ASIC-TV*)

Payment of **Rs. 679** may be made.

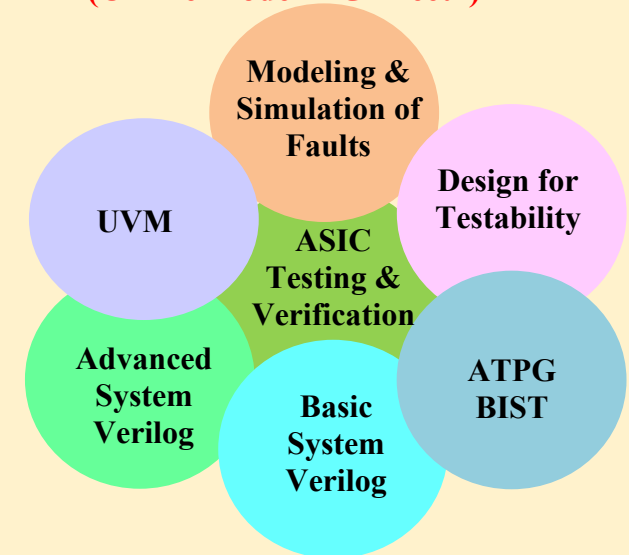
After Fee payment, may please proceed to register for the workshop using the link: [REGISTRATION FORM](#)

Last date for Registration: 5th March 2022.

National Workshop on ASIC – Testing and Verification

(06th to 10th March 2022)

(Online Mode in G-Meet*)



Organized by



Department of Electronics and Communication
Engineering
in Association with IEEE Student Branch,
IIITDM Kancheepuram

Course Coordinator

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**Link will be shared with the Registered Participants*