



IEEE REGION 10 SYMPOSIUM (TENSYP) 2020

5th – 7th June, 2020 | Dhaka, Bangladesh

IEEE TENSYP 2020 BEST PAPER AWARD

(VLSI, Circuits and Systems Track)

is presented to the paper

“PAPER ID: 1084; A Novel Method to Implement STDP Learning Rule in Verilog”

authored by

Amrutha Manoharan, Gadamsetty Muralidhar, and Binsu J Kailath

at the Full Virtual Conference **IEEE TENSYP 2020**.

Prof. Dr. Shaikh A. Fattah
TPC Chair, TENSYP 2020



Prof. Dr. Celia Shahnaz
General Chair, TENSYP 2020

